REPORT DOCUMENTATION PAGE

Form Approved
OMB No. 0704-0188

Public reporting burden for this collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Washington Headquarters Services, Directorate for information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302, and to the Office of Management and Budget, Paperwork Reduction Project (0704-0188), Washington, DC 20503.

1. AGENCY USE ONLY (Leave blank)	2. REPORT DATE	3. REPORT TYPE AND DATES COVERED		
<u> </u>	3/30/98	Annual Techr	Annual Technical 3/1/97 - 2/28/98	
4. TITLE AND SUBTITLE A Theoretical Probe into the Electronic, Geometric, and Dynamic Properties of Semiconductors 6. AUTHOR(S)			5. FUNDING NUMBERS N00014-97-1-0545	
Prof. John J. Joanopoul	os			
7. PERFORMING ORGANIZATION NAME Research Laboratory of Massachusetts Institu 7.7 Massachusetts Aver Cambridge, MA 02139	of Electronics ute of Technology		8. PERFORMING ORGANIZATION REPORT NUMBER	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES) Office of Naval Research Ballston Centre Tower One 800 North Quincy Street Arlington, VA 22217-5660			10. SPONSORING/MONITORING AGENCY REPORT NUMBER 97PR05049-00	
11. SUPPLEMENTARY NOTES The view, opinions and/or findings contained in this report are those of the author(s) and should not be construed as an official Department of the Army position, policy, or decision, unless so designated by other documentation.				
Approved for public rel	TEMENT		12b. DISTRIBUTION CODE	

13. ABSTRACT (Maximum 200 words)

For the past several years there has been a major international effort [1–4] on the heteroepitaxial growth of compound tetrahedrally coordinated semiconductors on Si substrates by MBE, MOCVD, etc.; on the fabrication of devices and circuits in these layers; and on the monolithic integration of such components with Si circuits fabricated on the same wafer. This effort is based on the significant potential that epitaxial growth of dissimilar semiconductor materials holds for technological applications. Nevertheless, relatively little theoretical work has been performed to understand the fundamental interactions and global issues governing the initial stages of growth and the structure of the first few mono-layers in these systems.

BJECT TERMS	D TIC Q	15. NUMBER OF PAGES 16. PRICE CODE	
17. SECURITY CLASSIFICATION OF REPORT	18. SECURITY CLASSIFICATION OF THIS PAGE	19. SECURITY CLASSIFICATION OF ABSTRACT	20. LIMITATION OF ABSTRACT
UNCLASSIFIED	UNCLASSIFIED	UNCLASSIFIED	UL

NSN 7540-01-280-5500

9980417 14

Standard Form 298 (Rev. 2-89) Prescribed by ANSI Std. 239-18 298-102

Technical Report for ONR Grant No. N00014-97-1-0545

For the past several years there has been a major international effort [1–4] on the heteroepitaxial growth of compound tetrahedrally coordinated semiconductors on Si substrates by MBE, MOCVD, etc.; on the fabrication of devices and circuits in these layers; and on the monolithic integration of such components with Si circuits fabricated on the same wafer. This effort is based on the significant potential that epitaxial growth of dissimilar semiconductor materials holds for technological applications. Nevertheless, relatively little theoretical work has been performed to understand the fundamental interactions and global issues governing the initial stages of growth and the structure of the first few mono-layers in these systems.

Of specific interest, for example, are the prototypical optically active systems GaAs on Si(100) and GaN on SiC/Si [6,7]. At present, optoelectronics involves growth of materials like GaAs (an optical material because of its direct band-gap) on substrates of Si (an electronic material with an indirect band-gap). For the future, GaN is of particular interest for optoelectronics applications in the blue and near UV because of its *direct* wide band gaps [5]. Unfortunately, the large lattice constant mismatches between the substrates and the epitaxial layers cause many defects to be created and propagate from the interface (see Table 1). Moreover, this is exacerbated by the interface charge mismatch caused by polarity differences between GaAs and Si. To resolve both problems, we invented a novel material specifically designed to consist of four-to-six types of atoms which *on the average* behave like Ga or As, by a systematic *ab-initio* exploration of the geometric, electronic and optical properties of a new class of compound semiconductors. The flexibility in the choice of ordering and sizes of atoms is used to (1) match the polarity of the substrate surface, (2) eliminate the

Table 1: Typical materials used in the electronics industry and their experimental lattice constant mismatch relative to Si $\Delta a/a_{Si}$ [8]. Most of the materials have a large lattice constant mismatch with Si. For those with a small lattice constant mismatch, there is still the problem of polarity mismatch.}

Compound	$\Delta a/a_{\rm Si}[\%]$	Polar	
Si	0	no	
Ge	+4.16	no	
CuCl	-0.46	yes	
ZnS	-0.41	yes	
GaP	+0.37	yes	
AlAs	+3.48	yes	
GaAs	+4.11	yes	
ZnSe	+4.36	yes	
InP	+8.06	yes	
InAs	+11.14	yes	

lattice mismatch and (3) tune other properties, such as obtaining a direct bandgap.

In cases where two tetrahedrally coordinated compounds can not grow directly on top of each other because of large lattice mismatches and/or polarity mismatches, a set of these new materials of gradually changing lattice constants can serve as buffer layers to bridge between the two target materials.

The central piece to the new material is an epitaxial ordering scheme that ensures polarity matching between the new material and any substrates. The procedure starts with a tetrahedrally coordinated homopolar substrate, such as Si(100). A layer of group V elements is then deposited on top of it, followed by a layer of group II elements, then a layer of group V elements and finally a layer of group IV elements. This completes a cycle of elements in the growth direction, from group IV, to group V, to group II, to group V, and back to group IV. The procedure can then be repeated. The electrostatic potential in the growth direction has the same periodic structure. Thus, there is no long range field effect, in contrast to the case of growing III-V material on top of group IV substrate (see Figure 1). Hence, the charge mismatch problem of the interface is eliminated. Furthermore, there is much flexibility on the types of atoms one can use. In principle, each layer can be of a different element, as long as it belongs to the correct group, this gives the designer many choices in adjusting for other physical properties. This new material can be written in a short-hand chemical formula (II-IV)_{1/2}V, while it is understood that there may be more than one component to any of the three groups. For example, given group II atoms Zn, Cd, group IV atoms Si, Ge, and group V atoms P, As, one could form compound ... Si P Zn As Ge As Cd As Si We will denote this class of material as Class I.

To achieve the same effect, we may also follow a different combination. We may start the deposition with a layer of group III element, followed by group

VI, group III, and then back to group IV. Written in chemical formula, this is III(IV-VI)_{1/2}, which we will call Class II.

Both Class I and Class II solve the polarity mismatch problem. It is clear that combinations of the two classes may also be considered.

substrate - Class I / Class II - Class I / Class II - ...

Moreover, the procedure is not limited to homopolar group IV substrates. When some heteropolar substrate is used, such as GaAs, we can simply start the procedure from the appropriate layer. For example, the layering order can be

... III V III V IV V II V IV ...

or

... III V III V II V IV V II V IV ...

These procedures leave the material designers with many choices. Different types of atoms and different layering orders can all play a role in influencing the lattice constant and other properties of the resulting material. Since some materials will certainly be easier to fabricate than others, it is also important to accumulate a large pool of candidate materials for various lattice constants and other properties.

We are currently performing *ab-initio* total energy calculations, as well as *ab-initio* quasiparticle energy calculations, in order to discover the specific choices of atoms necessary to identify the optimal materials for heteroepitaxial growth on Si.

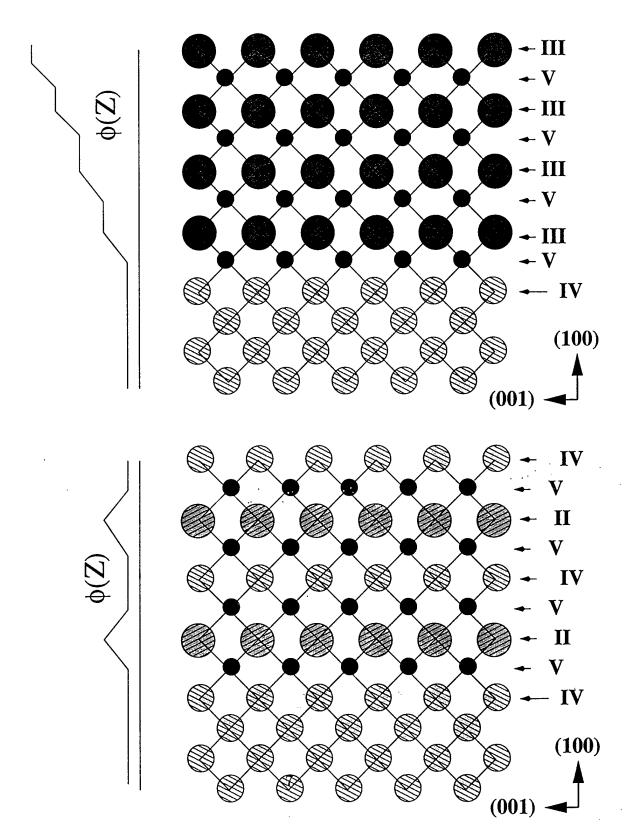
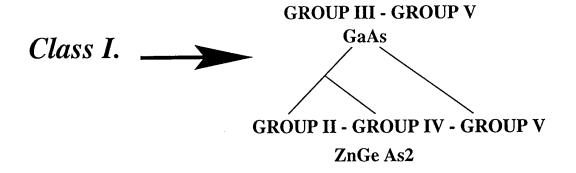


Figure 1: Illustration of epitaxial layering of Class I materials, compared with III-V materials. Note that the new material does not have long range field effect, in contrast to III-V materials.



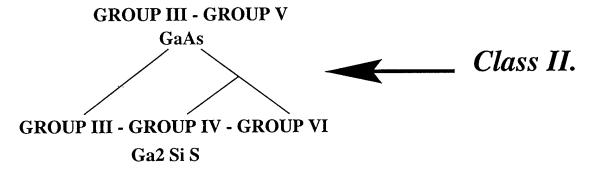


Figure 2: Class I materials are composed of group II, IV, V elements. One example is $ZnGeAs_2$. Class II materials are composed of group III, IV, VI elements. One example is Ga_2SiS .

References

- 1. Heteroepitaxy in Silicon II, ed. J. Fan, J. Phillips and B.-Y. Tsaur, MRS Symposia Proceedings N. 91 (Materials Research Society, Pittsburgh, PA, 1987).
- 2. R. F. Davis, *Proc. IEEE* **79**, 702 (1991).
- 3. J. H. Edgar, J. Mater. Res. 7, 235 (1992).
- 4. C. R. Eddy, Jr., T. D. Moustakas and J. Scanlon, J. Appl. Phys. 73, 448 (1993).
- 5. For a good review on GaN and other wide band-gap semiconductors, see J. E. Edgar, *M. Mater. Res.* **7**, 235 (1992).
- 6. T. Sasaki and T. Matsuoka, J. Appl. Phys. **64**, 4531 (1988).
- 7. Z. Sitar, L. L. Smith, and R. F. Davis, J. Cryst. Growth 141, 11 (1994).
- 8. M. L. Cohen, Electronic structure and optical properties of semiconductors

ATTACHMENT NUMBER 1

REPORTS AND REPORT DISTRIBUTION

REPORT TYPES

- (a) Performance (Technical) Report(s) (Include letter report(s)) Frequency: Annual
- (b) Final Technical Report, issued at completion of Grant.NOTE: Final Technical Reports must have a SF-298 accompanying them.
- (c) Final Financial Status Report (SF 269)
- (d) Final Patent Report (DD 882)

REPORTS DISTRIBUTION				
ADDRESSEES	REPORT TYPES	NUMBER OF COPIES		
Office of Naval Research Program Officer Larry R. Cooper ONR 312 Ballston Centre Tower One 800 North Quincy Street Arlington, VA 22217-5660	(a) & (b) w/(SF-298's)	3		
Administrative Grants Officer OFFICE OF NAVAL RESEARCH REGIONAL OFFICE BOSTON *	(c), (d) & SF- 298's only for (a) & (b)	1		
495 SUMMER STREET ROOM 103 BOSTON, MA 02210-2109				
Director, Naval Research Laboratory Attn: Code 2627 4555 Overlook Drive Washington, DC 20375-5326	(a) & (b) w/(SF-298's)	1		
Defense Technical Information Center 8725 John J. Kingman Road STE 0944 Ft. Belvoir, VA 22060-6218	(a) & (b) w/(SF-298's)	2		
Office of Naval Research Attn: ONR 00CC1 Ballston Centre Tower One 800 North Quincy Street Arlington, VA 22217-5660	(d)	1		

If the Program Officer directs, the Grantee shall make additional distribution of technical reports in accordance with a supplemental distribution list provided by the Program Officer. The supplemental distribution list shall not exceed 250 addresses.

^{*} For report types (a) and (b), send only a copy of the transmittal letter to the Administrative Contracting Officer; do not send actual reports to the Administrative Contracting Officer.